Amendments to the Specification:

Please replace the paragraph beginning on page 7 line 15 with the following amended paragraph:

--Referring to Figs. 2 and 3, the critical direction that requires the most scrutiny is the direction from unencrypted data to the encrypted data, as previously discussed. Data **1100** can enter the Separation Logic **1200**, which can be controlled by the Separation State Machine (M/C) A **1210**. (Step 300). A state machine **1310** is connected to the separation state machine A **1210** and controls the cryptographic component **1500** as described hereinafter.--

Please amend the paragraph beginning on page 8 line 3 with the following amended paragraph:

--The data can pass through a FIFO component **1150** to the cryptographic component **1500**, and the header information can pass to the Validation Logic **1400**, which can be controlled by the Validation State M/C A **1410**. The validation logic **1400** supplies a valid header to door A logic **1450**, which is coupled to door B logic **1650**. Door A logic **1450** transfers the valid header to door B logic **1650** under control of the validation state machine logic A **1410** and validation state machine logic B **1610**, respectively. For example, this logic can inspect the header information to determine whether its contents are valid. The Validation State M/C A **1410** can also, for example, validate the frequency of the header. (This information can also be contained in the Security Policy.) If the header information can be validated, Validation State M/C A **1410** can signal the Validation State M/C B **1610** that it has a valid header to transfer. (Step 315)